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For Immediate Release

Cypress Samples World's First 2.5 Gbps Programmable PHY

SERDES Chip Combines Programmable Logic, High-Speed Serial Interface Technology and Abundant Communications Memory, Targeting InfiniBand™ and Backplane Networking Applications

SAN JOSE, Calif., April 3, 2001 – Cypress Semiconductor (NYSE: CY) today announced the availability of samples of the PSI2G100, the first in its family of Programmable Serial Interface (PSI™) chips. The PSI2G100 combines a 2.5 Gbps serial link, 100k gates of programmable logic, and 240 Kbits of communications memory, targeting system backplanes across a broad range of market segments, including InfiniBand™.

PSI devices combine the flexibility, predictable timing, and ease-of-use of Cypress CPLDs with a SERDES, communications memory and phase-locked loops (PLLs). Cypress's *Warp*™ software enables a seamless programming interface to allow design engineers to easily integrate custom IP with the SERDES via HDL blocks, HDL text, or graphical state machines. Cypress is the only company to offer a 2.5 Gbps SERDES, programmable logic gates, design entry, synthesis and verification in an integrated, single-chip solution.

"Cypress is delivering a programmable SERDES with all the advantages of our CPLDs: high speed, predictable timing, flexibility, ease of use, and non-volatility at leadership densities," said Geoff Charubin, director of marketing for Cypress's data communications division. "The PSI2G100 is precision tuned for custom backplane applications, giving communications solutions designers the ability to interface with their existing processors, ASICs and insert their own custom logic."

"Cypress is first to market with the PSI2G100, ahead of both pure-play data communications/physical-layer companies and programmable-logic specialists," said Ralph Schmitt, Cypress vice president of sales and marketing. "Those companies will need to find partners or make acquisitions to achieve the same level of integration and customization that Cypress can deliver today."

The PSI2G100 has abundant communications memory, 48 Kbits of Dual Port/FIFO and 192 Kbits of single-port RAM for memory-intensive networking functions like queuing, head-of-line blocking and distributed switching. InfiniBand compliant, the PSI2G100 is a key component in Cypress's PSI family of backplane solutions. A subset of the family will be SONET-compliant and targeted at the OC-48 market.

The devices in the PSI family provide a programmable interface to a SERDES that is compatible with various physical layer transmission media – fiber optic modules, copper cables and circuit board traces. Along with optimized communications memory (such as dual-ported and FIFO memories), logic and PLLs, the ICs will provide multiple parallel I/Os supporting LVCMOS, LVTTTL, 3.3 Volt PCI, SSTL2, SSTL3, HSTL, and GTL+ inputs. The serial links will offer operating speeds from 1 x 2.5 GHz to 8 x 1.5 GHz to support high-bandwidth applications. The combined serial bandwidth of 200 Mbps to 12 Gbps will allow PSI devices to meet the requirements of a broad range of market segments.

Pricing and Availability

The PSI2G100 is the first PSI family product. Sampling now, the PSI2G100 is available in a 456-ball BGA package. Volume pricing for the 2.5 Gbps, 100k-gate, devices – slated for production in Q1 2001 – starts at \$80. For more information please go to <http://www.cypress.com/psi>.

About Cypress

Cypress Semiconductor is “Driving the Communications Revolution”™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-performance communication devices, high-density programmable logic devices; timing technology solutions; and controllers for Universal Serial Bus (USB).

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,500 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide Web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810).

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